

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

21995

Mukesh K Patel

Java Hardware Accelerator Using Microcode Engine

Docket No.:

000006.P001X

Filed:

October 13, 2000

Examiner:

Chameli C. Das

Serial No.: 09/687,777

Due Date: N/A

Group Art Unit: 2122

**MS** Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard and this transmittal document.

 $\underline{X}$  A Revocation and Power of Attorney (2 Pages).

X A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (2 pgs.), and copies of cited non-patent documents.

X A check in the amount of \$180 to cover the fee for consideration of Information Disclosure Statement under 97(c).

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Ву

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 157 day of June 2005.

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06/17/2005 JBALINAN 00000079 09687777

01 FC:1806

180.00 OP

S/N 09/687,777 **PATENT** 

IN THE UNITED STATES PATENT AND <u>TRADEMARK OFFICE</u>

Mukesh K Patel Examiner:

Chameli C. Das

Group Art Unit: 2122

October 13, 2000 000006.P001X Docket:

Title: Java Hardware Accelerator Using Microcode Engine

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

09/687,777

proficant:

al No.:

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 503437.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

In accordance with the Patent and Trademark Office's waiver of the requirement under 37 C.F.R.§ 1.98(a)(2)(i), copies of the listed U.S. patents are not enclosed because the current application has been filed after June 30, 2003.

In accordance with the waiver of 37 C.F.R.§ 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and publication for all U.S. national patent applications filed after June 30,

2003, only copies of the foreign references and other documents are enclosed. 06/17/2005 JBALINAN 00000079 09687777

01 FC:1806 180,00 OP

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :09/687,777

Filing Date: October 13, 2000

Title: Java Hardware Accelerator Using Microcode Engine

Page 2 Dkt: 000006.P001X

Respectfully submitted,

MUKESH K PATEL

By his Representatives,

Hahn and Moodley LLP Suite 180 CA 94040

Date June 15, 2005

Vani Moodley

Reg. No. 56631

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Complete if Known Substitute for form 1449A/PTO INFORMATION DISCLOSURE **Application Number** 09/687,777 STATEMENT BY APPLICANT (Users) many sheets as necessary) **Filing Date** October 13, 2000 **First Named Inventor** Patel, Mukesh JUN 1 7 2005 **Group Art Unit** 2122 **Examiner Name** Das, Chameli Attorney Docket No: 000006.P001X eet 1 of 3

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